



SIE2017

49TH ANNUAL MEETING OF
THE ASSOCIAZIONE SOCIETÀ
ITALIANA DI ELETTRONICA

Sponsored by:



The SIE2017 Annual Meeting
is hosted in Palermo at
Palazzo Chiamonte Steri,
piazza Marina, 61.

Contacts

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www.unipa.it/elettronica

*Shaping our future with
Electronics*

The SIE2017 Annual Meeting brings together
electronic engineering academics and PhD
students from all the Italian Universities.



Wednesday, June 21

8:30	Registration	
9:00-11:00	Exhibitors' showcases	
11:00-13:00	SIE Scientific Council	
13:00-14:00	LUNCH*	
14:00-14:30	Welcome Address	
	Opening Speech <i>Andrea Lacaita,</i> <i>SIE President</i>	
14:30-15:10	IEEE Distinguished Lecture <i>Simon Deleonibus</i> <i>IEEE Electron Devices Society</i>	
15:10-16:30	Oral Sessions	
	Micro- and Nano- Electronic Devices	Electronic Systems and Applications I
16:30-17:00	COFFEE BREAK	
17:00-19:00	Oral Sessions	
	Microwave Electronics	Sensors, Microsystems and Instrumentation I

* Only for SIE Scientific Council Members and Sponsors

21:00

WELCOME DINNER

Thursday, June 22

8:30-9:10	Invited Lecture <i>Raphaël Buttè</i> <i>EPFL, Switzerland</i>	
9:10-9:50	Invited Lecture <i>Richard M. De La Rue</i> <i>University of Glasgow, UK</i>	
9:50-10:30	Poster Session**	
10:30-11:00	COFFEE BREAK	
11:00-13:00	Oral Sessions	
	Power Electronics	Sensors, Microsystems and Instrumentation II
13:00-14:00	LUNCH	
14:00-14:40	Invited Lecture <i>Massimo Alioto</i> <i>NUS, Singapore</i>	
14:40-15:10	Poster Session**	
15:10-16:30	Oral Sessions	
	Integrated Circuits and Systems I	Optoelectronics and Photonics
16:30-17:00	COFFEE BREAK	
17:00-19:00	Oral Sessions	
	Integrated Circuits and Systems II	Electronic Systems and Applications II

** Posters will remain on display during the whole conference

21:00

GALA DINNER

Friday, June 23

8:50-9:30	IEEE Fellow Lecture <i>Salvo Pennisi</i> <i>University of Catania</i>
9:30-10:10	IEEE Fellow Lecture <i>Carlo Samori</i> <i>Politecnico di Milano</i>
10:10-10:30	Recognitions and Awards
10:30-11:00	COFFEE BREAK
11:00-11:30	ECSEL Program Presentation <i>Yves Gigase</i> <i>ECSEL JU Head of Programme</i>
11:30-12:00	InSSIght Project Presentation <i>Giancarlo Forlanini</i> <i>ECSEL IT President</i>
	
12:00-13:30	Smart System Integration for ECSEL Program Round Table on Funding for Smart Systems & ECSEL
13:30-14:30	LUNCH
14:30-16:00	SIE General Assembly

16:00

SOCIAL TOUR